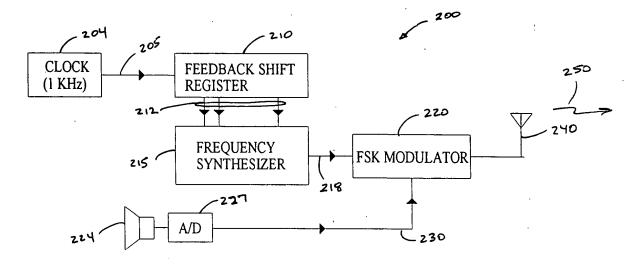


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F16.2

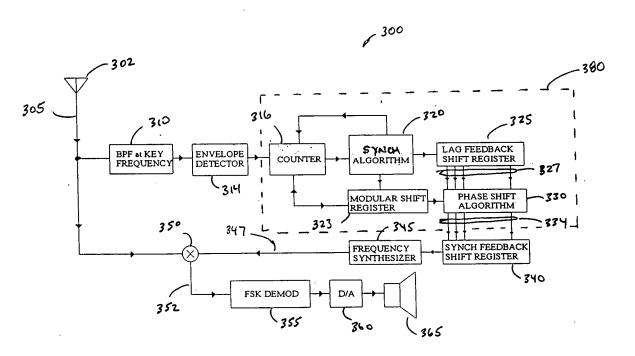
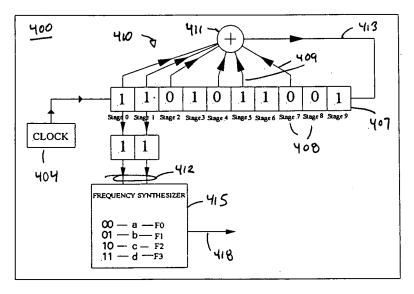


FIG. 3



Ten Stage Hopper Configuration

FIG. 4

```
503 — 0123456789012345678901234567890... time intends

504 — 110101100110111111111111000110001... code

510 — ddcbcbdcabdcbdddddddddaabdcaab...sequence of frequencies

515 — 0 4 789012 relative times of arrival

Code (8) and Associated Sequence of Frequencies
```

FIG. 5

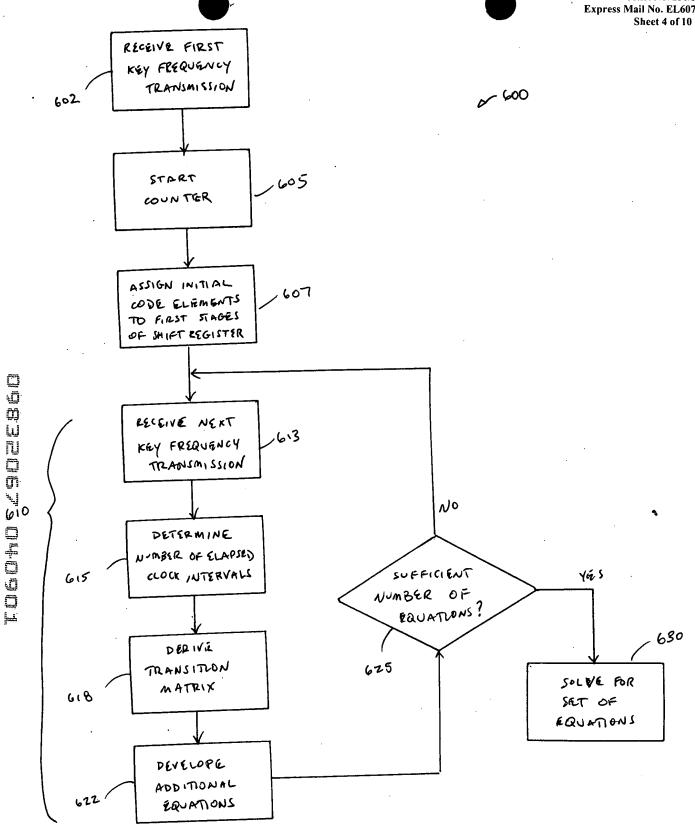


FIG. 6

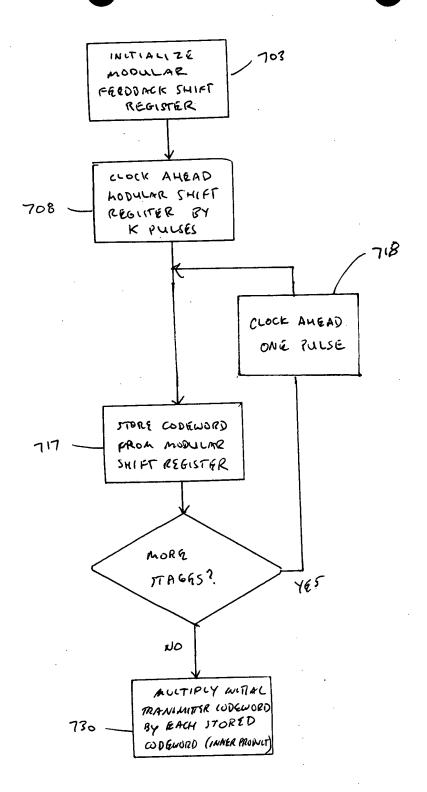
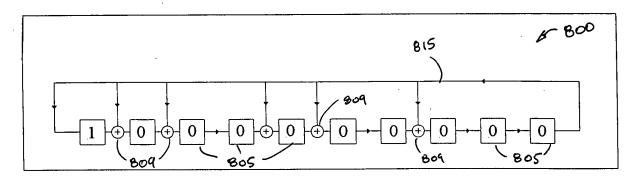


FIG. 7



Ten Stage Modular Register

FIG. 8

FIG. 9

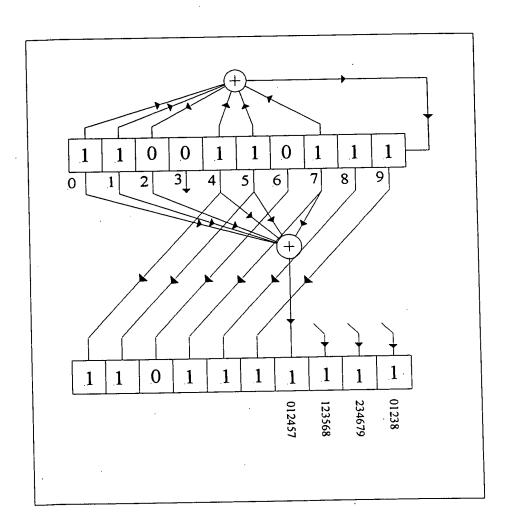
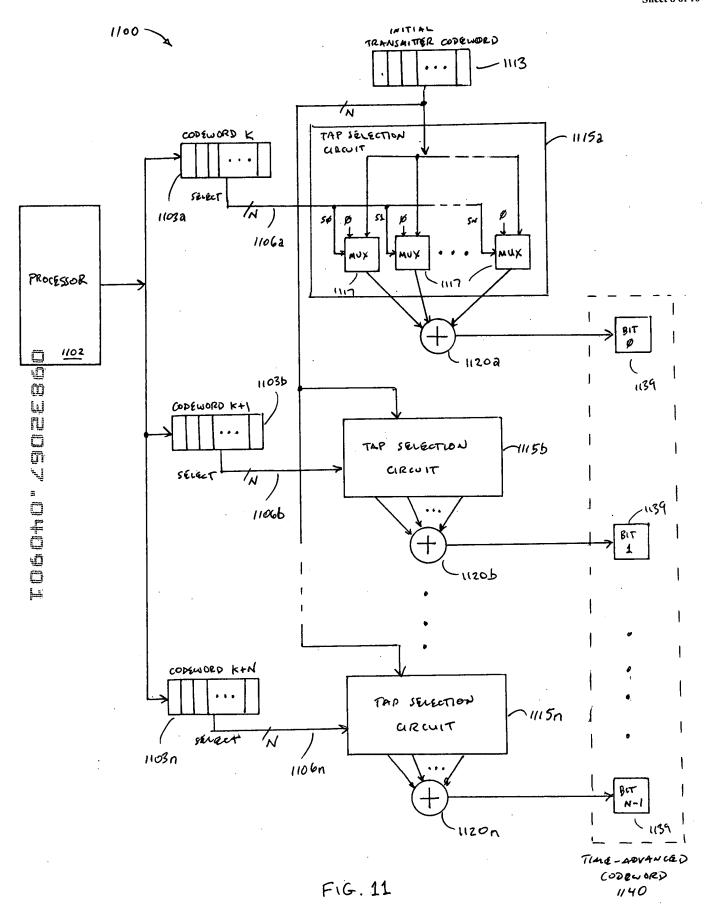
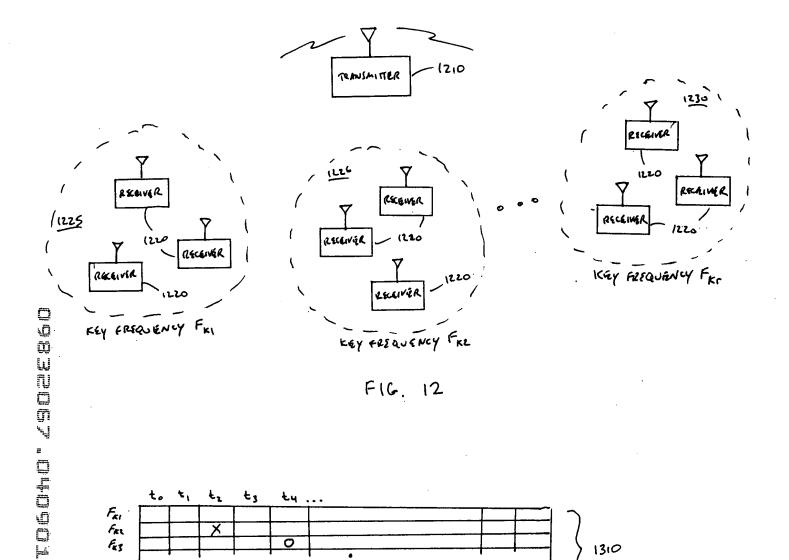


FIG. 10





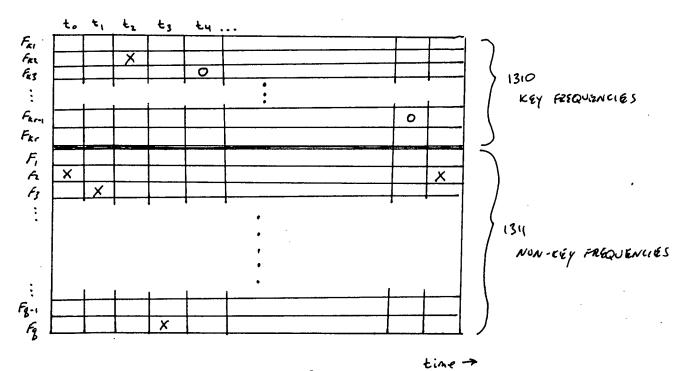


FIG. 13

